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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)			Application Number	10/801,588	
			Filing Date	March 17, 2004	
			First Named Inventor	Soeparto Tandjoeng	
			Art Unit	N/A	
			Examiner Name	Not Yet Assigned	
Sheet	1	of	1	Attorney Docket Number	M4065.0673/P673

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS						
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NON PATENT LITERATURE DOCUMENTS			
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YW	CA	Data Sheet: "Synchronous DRAM," Micron Technology, Inc. © 2002, Micron Technology, Inc.	
YW	CB	Digital Integrated Circuits, A Design Perspective Chapter 10, "Designing Memory and Array Structures," © 1996 Prentice Hall, Inc., pp. 551-583	
YW	CC	Testing Semiconductor Memories, Theory and Practice, A.J. van de Goor, Chapter 4, "Functional RAM chip testing", © 1991 John Wiley & Sons, pp. 93-105	

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/Yolanda Wilson/

12/28/2006